



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

13/IDS
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9/3/02

Application Serial No.	09/559,903
Filing Date	April 26, 2000
Inventor	Zhiping Yin et al.
Assignee	Micron Technology, Inc.
Group Art Unit	2815
Examiner	G. Eckert II
Attorney's Docket No.	MI22-1427
Title:	Circuitry and Gate Stacks

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

To: Assistant Commissioner for Patents
Washington, D.C. 20231

From: Jennifer J. Taylor, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

Dear Sir:

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449 and a copy of which are attached.

Citation of these references is respectfully requested.

A check in the amount of \$180.00 is enclosed to cover the fee specified under 37 C.F.R. § 1.17(p).

Respectfully submitted,

Dated: August 23, 2002 By: Jennifer J. Taylor
Jennifer J. Taylor, Ph.D.
Reg. No. 48,711

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